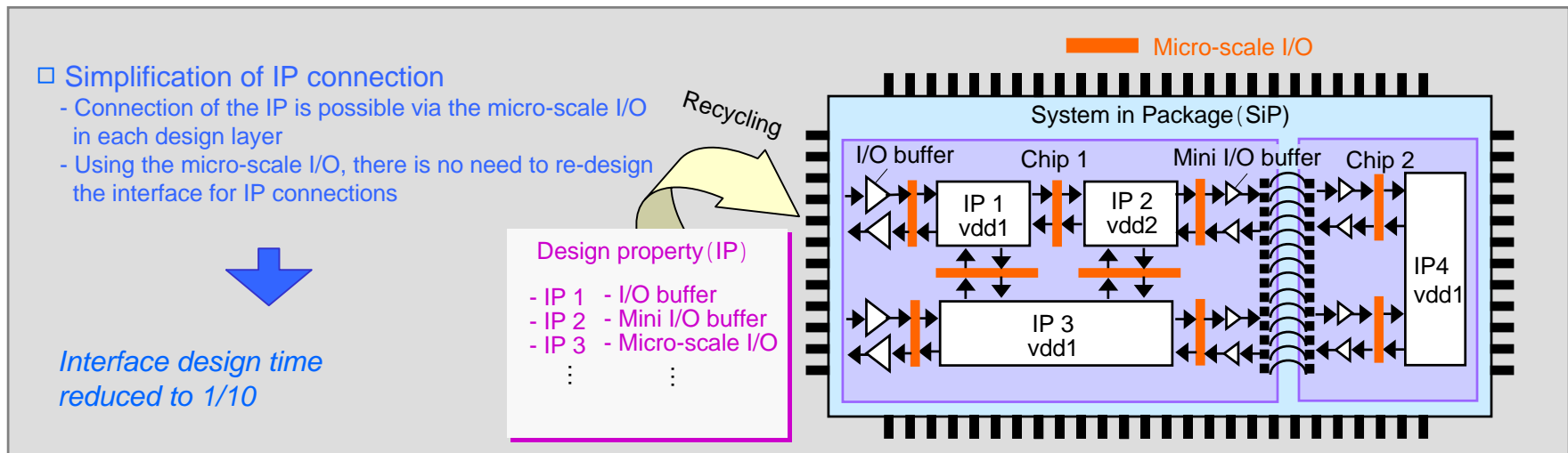


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Micro-scale I/O architecture for reducing design time & power consumption in system LSIs - Simplifying IP (Intellectual Property) Re-use -



Micro-scale I/O (input/output interface) architecture, a new concept circuit design technology, was developed to enable LSI designers to easily re-use previously designed logic circuit modules (IP: Intellectual Property). By using this technology, a variety of circuits with different power supply voltages and fabrication processes can be easily integrated in one system LSI or in a single package with independent power switching control for each IP in order to achieve low power consumption. This technology is expected to contribute to the reduction of interface design time when reusing IP and yields a lower power consumption.